

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP04903G/554484

Dated:27/07/2026

CORRIGENDUM

Advertisement No.: IITD/IRD/191/2026

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत परियोजना की नियुक्तियों के लिए भारतीय राष्ट्रीयता के उम्मीदवारों को वॉक-इन परीक्षा/साक्षात्कार के लिए उपस्थित होने के लिए आमंत्रित किया जाता है। अपॉइंटमेंट वार्षिक आधार पर या परियोजना की अवधि तक समेकित वेतन पर सविदात्मक आधार पर जो भी पहले हो, होगा।

Title of the Project	Design and Demonstration of a Highly Scalable Quantum Computer using Semiconducting Qubits. (RP04903G)	
Funding Agency	Department of Science and Technology (NQM)	
Name of the Project Investigator	Prof. Abhisek Dixit email ID: team.dwlcl@gmail.com]	
Deptt./Centre	Dept. of Electrical Engineering	
Duration of the Project	Upto: 31/03/2031	
Post (s)	Consolidated Pay-slab / Fellowship	Qualifications
Project Scientist -(02)	Rs.56,000 - 67,000 – 78,000/- p.m. plus HRA @27%	Doctoral Degree in Electronics Engineering or Electronics or Physics with specialization in Electronics. Expertise should be in analog/RF design & characterization or silicon quantum devices. Depending on the years of post-PhD experience, starting level and increments will be decided. Experience of handling large projects is also desirable. *Post PhD experience & performance-based increments as per DST norms.
Walk In Test / Date of Interview	Time of walk-in-Interview	Venue
07/08/2026	10:00 A.M.	In the Committee Room of Department of Electrical Engineering, Block – II, Academic Area, IIT Delhi, Hauz Khas, New Delhi – 110 016

The candidates who fulfill the above qualifications/experience should appear for the interview. Kindly bring your formal application on Form No. IRD/REC-4, which can be downloaded from IRD Website (<http://ird.iitd.ac.in/rec>) through proper channel along with complete information regarding educational qualifications indicating percentage of marks of each examination passed, details of work experience and a recent passport size photograph, along with original certificates (both academic & professional) for verification on the date of interview. No candidate, who is already employed at the Institute / IRD shall be interviewed unless his/her application has been duly forwarded by their concerned establishment/sections. 5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। In case any clarification is required on eligibility regarding the above post, the candidate may contact at e-mail id: team.dwlcl@gmail.com

कार्यवाहक प्रमुख (आईआरडी स्थापना)

वितरण

- Head of the Deptt./Centres/Units : Deptt./Centre/Unit
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- Prof. Abhisek Dixit, PI, Dept. of Electrical Engineering

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